### 18th MORNING SCHEDULE



### **Registration** 08:30 AM

**Management Message:** 

09:30 - 10:00 AM Room : Hall 1



**Mr Jean Bélanger**Founder, President & CTO
OPAL-RT Technologies



**Mr Girish Nanjundaiah**Managing Director
OPAL-RT Technologies India

### **Keynote Sessions**

Digital Twin for the Indian Power System

10:00 - 10:20 AM



**Mr Reji Kumar Pillai**President
India Smart Grid Forum (ISGF)

HILS using OPAL-RT: A 15-Year Review of Advancements, Advantages, and Applications

10:20 - 10:40 AM



**Mr. Pulak Halder** Scientist RCI Hyderabad

**Using OPAL-RT HIL Benches in ARAI** 

10:40 - 11:00 AM



### High Tea / Demo Visit 11:00 - 11:20 AM

**EV OS - Journey into the SDV universe** 

11:20 - 11:40 AM



**Dr Himadri Bhushan Das** Chief Solution Architect Electra EV

Accelerating India's Energy Transition: Policy Strategies for Renewable Integration and Grid Decarbonization

11:40 - 12:00 AM



**Mr Arun Kumar Choudhary**Scientist
Ministry of New and Renewable Energy

### **Technical Sessions**

Latest Innovations for Power Electronics & eMobility (Traction, BMS and OBC) HIL Testing

12:00 - 12:30 PM



**Mr François Berthelot**BU Manager, Eastern North America
OPAL-RT Technologies

Securing the Smart Grid: Leveraging Simulation and Visibility for Cyber-Resilient Power Systems

12:30 - 01:00 PM

Mr Sunny Nair

Innovative Solutions & Cybersecurity Architect Keysight Technologies

How OPAL-RT Customized Test Benches contribute to customer(s) success

01:00 - 01:30 PM



**Mr Sameer Singh** Technical Head OPAL-RT Technologies india

# RT24

## 18th AFTERNOON SCHEDULE

### User Presentations 02:30 - 04:00 PM

**Bridging Theory and Practice: Real-Time Testing of Electric Motor and Generator Controllers** 

**Mr. Muthukumar Murthy**Systems Engineering Manager
Honeywell Technology Solutions

**Revolutionizing EV Batteries: The Power of Software- Defined BMS** 

**Dr Mukesh Singh**Professor
Thapar Institute of Engineering and Technology

Genset controller software functional testing on OPAL-RT bench for multiple topologies

**Ms. Pragati Raut & Mr. Subodh Kadu** Test System Engineers Cummins India Pvt. Ltd.

**Using OPAL-RT for building Digital Twins** 

**Dr Amitava Gupta**Professor (Power Engg.) & Director (SNSA)
Jadavpur University

### **Technical Sessions**

A Platform-Based Approach for DC Fast-Charging Systems Test Solutions

02:30 - 03:00 PM

Mr Karthik SP Section Manager -JKAI National Instruments (NI) **Turnkey Training Platforms for Power generation & Distribution** 

03:30-04:30 PM



**Mr Ashot Minasyan** Chief Technology Officer Bitlismen



**Mr Gevorg Parsyan**Systems Engineer
Bitlismen

**High Tea** 04:30 - 05:00 PM

**DEMO / New Office Visit** 05:00 - 06:00 PM Entertainment & Dinner 06:30 - 09:30 PM

**Power Transmission with Bitlismen** 



## **DEMONSTRATION DETAILS**

DEMOS
Dool Time Simulation of Unbrid CMDE and DMS for Automotive
Real-Time Simulation of Hybrid CMDE and BMS for Automotive
Future of Transportation : eVTOL Flight Simulation and Control Testing
Electric Vehicle HIL Testing of Power Train and On Board Charger
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Real-Time Flight Dynamics Simulation with Avionics Bus Integration
Cybersecurity readiness of the GHOST microgrid with HYPERSIM
Revolutionizing IBR Integration with Smart Inverter Control Toolbox
Revolutionizing ibk integration with smart inverter control roolbox
HIL Testing of PPC (Power Plant Controller) using OPAL-RT
Power-Hardware in Loop demonstration with OPAL-RT and Bitlismen
Salay Baway Canayatian with Bitliaman
Solar Power Generation with Bitlismen
Wind Power Generation with Bitlismen
Power Transmission with Bitlismen