RT24 INDIA - 17 & 18th of December 2024,
at Hyatt Centric, Hebbal, Bengaluru

17th MORNING SCHEDULE

RT24
ELECTRIFYING THE WORLD BEYOND REAL TIME

Registration 08:30 AM

Management Message :
09:15 - 09:40 AM
Room : Hall 1

Mr Jean Bélanger
Founder, President & CTO
OPAL-RT Technologies

Mr Girish Nanjundaiah
Managing Director
OPAL-RT Technologies India

Keynote Sessions

The Energy Transport Information Nexus
09:40 - 10:00 AM

Dr Siddhartha Mukhopadhyay
Professor
Indian Institute of Technology, Kharagpur

Cyber-Resilient Smart Grid Systems: Impact of Electric Vehicle Charging, Standards and Protocols
10:00 - 10:20 AM

Dr Rajeev Singh
Professor
Indian Institute of Technology (BHU)

Securing Critical Energy Infrastructure - A Cyber-Physical System (CPS) Perspective
10:20 - 10:40 AM

Dr Faruk Kazi
Professor
VJTI, Mumbai

Power Hardware in the Loop (PHIL) Simulations of Converter Interfaced Renewable Generation
10:40 - 11:00 AM

Dr Sarasij Das
Associate Professor
Indian Institute of Science (IISc)

High Tea / Demo Visit 11:00 - 11:30 AM

Technical Sessions

Securing the Smart Grid: Leveraging Simulation and Visibility for Cyber-Resilient Power Systems
11:30 - 12:00 PM

Mr Sunny Nair
Innovative Solutions & Cybersecurity Architect
Keysight Technologies

Simulation Tools & Test Benches: Advancing Research & Fostering Education
12:00 - 12:30 PM

Mr François Berthelot
BU Manager, Eastern North America
OPAL-RT Technologies

State-of-the-Art Labs & Research Platforms Academic R&D Consultancy
12:30 - 01:00 PM

Mr Ashot Minasyan
Chief Technology Officer
Bitlismen

Mr Gevorg Parsyan
Systems Engineer
Bitlismen

Networking Lunch / Demo Visit 01:00 - 02:00 PM

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17th AFTERNOON SCHEDULE

User Presentations 02:00 - 04:40 PM

Importance of WADC in Renewable Rich Power System: A Real-time Case Study Using OPAL -RT
Dr SK Parida, Associate Professor, Department of Electrical Engineering, **IIT Patna**

Coherency Identification and Prediction of Out of Step Condition in Power System
Dr S.T. Nagarajan, Professor, Department of Electrical Engineering, **Delhi Technological University**

Coordinated Power Management in DC Microgrid using OPAL-RT
Dr Vijaya Bhaskar, Associate Professor, **IIT (ISM) Dhanbad**

Real-Time Simulation of Microgrids with the Integration of Solar Photovoltaic (PV) and Battery Energy Storage System (BESS).
Dr PESN Raju, Assistant Professor, School of Energy Science and Engineering, **IIT- Guwahati**

Multilevel Converter Development: simulation to Power Hardware Testing with OPAL-RT
Dr. Abhijit Kshirsagar - Assistant Professor, **Mr. Abhishek (PhD)**, Department of Electrical Engineering, **IIT Dharwad**

Pulse Genration for Multilevel Inverter through OPAL-RT
Dr.J. Venkataramanaiah, Assistant Professor, Department of Electrical Engineering, **SVNIT - Surat**

Grid connected Multiport system using OPAL-RT
Dr. A. Hema Chander, Assistant Professor, Department of Electrical Engineering, **NIT Puducherry**

Advanced Control Strategies For Load Frequency Regulation In Renewable Energy
Dr Rajeswari R, Professor, Department of Electrical Engineering, **GCT - Coimbatore**

Hardware Prototype Implementation of Battery Charging System with DAB Converter along with Closed Loop Control
Dr Anbuselvi, Associate Professor, Department of Electrical Engineering , **Anna University**

Neural Network based Adaptive Control of DC-DC Power Converters
Dr Tousif Khan, Associate Professor , Electrical and Electronics Engineering, **SRM AP**

Research in VIT Chennai Using OPAL-RT
Dr. Nilanjan Tiwari, Associate Professor, School of Electrical Engineering, **VIT Chennai**

Power Hardware in the loop emulation of Permanent Magnet Synchronous machines
Dr. Arun Rahul S, Assistant Professor, Department of Electrical Engineering, **IIT Palakkad**

Wide-area Voltage Stability Assessment Using Loading Margin Sensitivity for Increasing Linear Load Levels and Wind Power Penetration.
Dr Raju Chintakindi, Associate Dean, Research & Industry Institute Interaction, **Vaagdevi College of Engineering**

An Overview of Power-Hardware-in-Loop based Experimental Validation Using OPAL-RT
Mr Aaqib Manzoor & Mr. Pratosh Patankar, Phd Student, **IIT Bombay**

Centralized Energy Management Scheme for Grid Connected AC/DC Microgrid
Chandrakant Bhatar, Research Scholar, **VNIT**

A Single Sensor-based Speed Sensorless Adaptive Step Size MPPT Control of a PMSG-based VSWECS
Mr.Balaji Mendi ,Alumni, **NIT Rourkela**

High Tea
04:40 - 05:00 PM

DEMO / New Office Visit
05:00 - 06:00 PM

Entertainment & Dinner
06:30 - 09:30 PM

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DEMONSTRATION DETAILS

DEMOS
Real-Time Simulation of Hybrid CMDE and BMS for Automotive
Future of Transportation : eVTOL Flight Simulation and Control Testing
Electric Vehicle HIL Testing of Power Train and On Board Charger
Real-Time Flight Dynamics Simulation with Avionics Bus Integration
Cybersecurity readiness of the GHOST microgrid with HYPERSIM
Revolutionizing IBR Integration with Smart Inverter Control Toolbox
HIL Testing of PPC (Power Plant Controller) using OPAL-RT
Power-Hardware in Loop demonstration with OPAL-RT and Bitlismen
Solar Power Generation with Bitlismen
Wind Power Generation with Bitlismen
Power Transmission with Bitlismen
Power Transmission with Bitlismen