

28th CONFERENCE SCHEDULE

Opening Session

Simon Abourida: OPAL-RT Technologies

Ashraf O P Ebrahim: D-SAT

08:30 - 09:00 AM





Cybersecurity Solutions for Future Energy Systems

09:00-09:30 AM



Mohd Asim Aftab Postdoctoral Fellow King Abdullah University of Science and Technology (KAUST)

RST Robust Control for DC-DC Buck Converter Feeding Constant Power Loads: HIL Testing

09:30-10:00 AM



Akram Mohammed PhD. Candidate King Saud University

Morning Break 10:00 - 10:30 AM

Enhancing Resiliency of a Low Inertia Power System under Large-scale Disturbances and Contingencies

10:30-11:00 AM



Muhammad Faroog Umar Postdoctoral Research Associate Texas A&M University at Qatar

Power-hardware-in-the-loop (PHIL) Simulations: **Utility in Product Development and Research**

11:00-11:30 AM



Amit Kumar KS Team Lead for PHIL Applications

Unlocking Innovation in Power Electronics: Hardware Design Secrets for Advanced Topologies 11:30-12:00 PM



Khalid Ahmad CEO & CTO <u>Taraz</u>

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Lunch Break 12:00-01:30 PM

Control and Hardware-in-the-Loop Simulation of Energy Storage and Electric Vehicle systems

01:30-02:00 PM



Talal Alharbi Professor Qassim University

PHIL Verification for Virtual Synchronous Generator Grid-Forming Control

02:00-02:30 PM



Muntathir Al Talag PhD Candidate Khalifa University

Dewesoft Innovative Approach to Complex Electrical and Mechanical Combined Test

02:30- 03:00 PM



Gabriele Ribichini DEWESoft[®]

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Afternoon Break 03:00-03:30 PM

DEWA's Real-time Testbed for Grid Modernization 03:30-04:00 PM



Gagandeep Dua Senior Researcher **Dubai Electricity and Water Authority** R&D Center

PONOVO's PHIL(Power-Hardware-In-Loop) 4-Quadrant **Power Amplifier and Testing Application**



Jennifer Liu General Manager-International Department

PONOVO

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Closeing Session OPAL-RT Technologies 04:30-05:00 PM