

28th MORNING SCHEDULE

Keynote : Electrifying the World Beyond Real-Time

08:30 - 09:00 AM

Room : Lowther



Jean Belanger
President and CTO
Opal-RT Technologies

Keynote : Pairing Wide Area Control and Real-time HiL Simulation for Zero Carbon Grid Technology

09:00 - 09:30 AM

Room : Lowther



Douglas Wilson
Chief Scientist
GE Vernova

Keynote : Subject TBD

09:30 - 10:00 AM

Room : Lowther



Yuan Chen
Model Development Manager
NESO

Morning Break: "Take a bite at the Expo"

10:00 - 10:30 AM

Room : Platform 5 Café

Microgrid & DC Grids

Room : Lowther

Using OPAL-RT Simulators for HIL Testing of MicroGrid Solutions - Practical Experience

10:30 - 11:00 AM

Oleg Bagleybter Senior Staff Engineering Manager
GE Vernova

Short-Term Control of Heat Pumps Supporting Power Grid Operation in Multi-Energy Systems.

11:00-11:30 AM

Daniele Carta Group Leader Energy Systems Engineering
Forschungszentrum Jülich GmbH

Subject TBD

11:30 - 12:00 PM

Bruce Ikenna Efika Head of Solution Development
BP Advanced Energy Solutions

Subject TBD

12:00-12:30 PM

First Name Last Name Job title
Company

Protection and Control for Power Systems

Room : Menteith

An Innovative Approach to the Controlled Switching of Compensated Transmission lines/cable

10:30 - 11:00 AM

Urmil Parikh Principal Engineer Power Systems and
Global Program Manager Controlled Switching
Hitachi Energy Sweden AB

Subject TBD

11:00-11:30 AM

Xiaowei Zhao Professor of Control Engineering
Mostafa Kheshti Assistant Professor
University of Warwick

Validation of Substation Automation using Real-time simulator: a Blackstart use case

11:00-11:30 AM

Ngoc Bao Lai Principal Controls Engineer
Fluence Energy GmbH

Use of HiL in Development of Zonal Autonomous Control for Power Systems

12:00-12:30 PM

Sean Norris Senior innovation application architect
GE Vernova

Lunch break with networking and visit of the demo & expo 12:30 - 02:00 PM Room : Platform 5 Café

28th AFTERNOON SCHEDULE

Power Hardware-in-the-Loop Room : Lowther

Flexible Laboratory Setups for Grid-Forming Control: Advances through RCP and P-HiL
02:00-02:30 PM
Jonas Steffen Group Leader
Fraunhofer IEE

Power HiL System for Impedance Scan
02:30-03:00 PM
Yue Zhu Research Associate
Imperial College London

PHIL Instability Prediction Using Frequency Domain Impedance Analysis and Prediction
03:00-03:30 PM
Frédéric Colas Research Engineer
L2EP/ENSAM

Inverter Based Energy Resources Room : Menteith

Subject TBD
02:00-02:30 PM
Michael Smailes Principle Research Engineer
ORE Catapult

Role of Hardware-in-the-Loop Testing for Fast Deployment of Grid-Forming Converters
02:30-03:00 PM
Sulav Ghimire Power Systems Engineer
Siemens Gamesa

TUTORIAL
Black-start in a highly IBR-penetrated grid
03:00-03:30 PM
Douglas Wilson Chief Scientist
GE Vernova

TRAINING

#1
HYPERMIM
Vithuran Vilvarajah
Team Lead for Support EU
Opal-RT Technologies

Afternoon Break: "Tea Time !" 03:30 - 04:00 PM Room : Platform 5 Café

Microgrid & DC Grids Room : Lowther

Advances in Real Time Simulation Technology and Techniques
04:00-04:30 PM
Ravinder Venugopal VP, BD and R&D EMEA
Opal-RT Technologies

TUTORIAL
An innovative approach to the Controlled switching of compensated transmission lines/cable
04:30-05:30 PM
Urmil Parikh Principal Engineer Power Systems and Global Program Manager Controlled Switching
Hitachi Energy Sweden AB

Power Electronics Room : Menteith

Success Story : Schneider Electric
04:00-04:30 PM
First Name Last Name Job title
Company

Computational power and flexibility to implement full system without core couplers
04:30-05:00 PM
Martin Butcher Consulting Engineer
GE Power Conversion

Using Opal-RT Simulators for Rapid Control Prototyping of Finite Control Set-MPC and AI-ba
05:00-05:30 PM
Paul Judge Reader in Power Electronics
Ning Li PhD Student
University of Edinburgh

OPAL-RT Technologies Gala Dinner After 07:00 PM



29th MORNING SCHEDULE

Keynote : Subject TBD

08:30 - 09:00 AM
Room : Lowther



Jean-Baptiste Heyberger
Architecture and System Director
Super Grid Institute

Keynote : National HVDC Centre-Experiences of Large Scale Real-Time Simulations, and Future Outlooking GB

09:00 - 09:30 AM
Room : Lowther



Ben Marshall
Technology Manager and Power System Engineer
The National HVDC Centre

Keynote : Subject TBD

09:30 - 10:00 AM
Room : Lowther



Prof. Willem Leterme
University Professor
RWTH Aachen University

Coffee Break

10:00 - 10:30 AM

Room : Platform 5 Café

HVDC, MMC & FACTS

Room : Lowther

MTDC Grid Reconfiguration - Sequential Control Principles and First Applications

10:30 - 11:00 AM

Lucas Molina Barros R&D Engineer
SuperGrid Institute

Co-simulation Opportunities for Large Area Models with HVDC Replicas

11:00-11:30 AM

Colin Foote Senior Simulation Engineer
The National HVDC Centre

SUCCESS STORY : The Use of Hypersim for HVDC Replicas - a Use Case with SIEMENS

11:30-12:00 AM

Benoit de Foucault Electrical Engineer
RTE international

TUTORIAL

Simplifying MMC Simulation

12:00-12:30 PM

Wei Li Team Lead Power Electronics
Opal-RT Technologies



Power Electronics

Room : Menteith

Subject TBD

10:30 - 11:00 AM

Agusti Egea Alvarez Professor
University of Edinburgh / SP Energy Networks

Railway traction system controllers

11:00-11:30 AM

Florent Chabrier Drivetrain Simulation Lead
SNCF Voyageurs

How to accelerate Power Electronics R&D cycle with PELab and OPAL-RT Real-Time Simulators

11:30-12:00 AM

Mubeen Abbas CEO
TechCore

Subject TBD

12:00-12:30 PM

Maroufi Seyede Masoome PhD Researcher
Karlsruhe Institute of Technology

Lunch break with networking and visit of the demo & expo 12:30 - 02:00 PM Room : Platform 5 Café

29th AFTERNOON SCHEDULE

TRAINING

Cybersecurity and Digital Twins

Room : Lowther

Keysight Overview of Cybersecurity

02:00-02:30 PM

Leslie Provenzano Experienced Sales and Marketing Leader
Kesight Technologies

TUTORIAL

Importance of Cybersecurity Simulation

02:30-03:00 PM

Steffen Vogel R&D & Software Engineer
OPAL-RT Technologies



#2

HYPERSIM

Vithuran Vilvarajah

Team Lead for Support EU
Opal-RT Technologies

Design of an adaptive digital twin to increase transparency in power grids

03:00-03:30 PM

Stephan Ruhe Research Associate
Fraunhofer IOSB-AST

Subject TBD

03:30-04:00 PM

Xin Zhang Professor
University of Sheffield

Digital substation HIL co-simulation, challenges and cybersecurity aspects

04:00-04:30 PM

János Csatár Senior Lecturer
Budapest University of Technology and Economics

Power Electronics

Room : Menteith

TUTORIAL

How to make the most out of your eHS / FPGA Simulation?

02:00-04:00 PM

Sebastien Cense Director - FPGA Simulation
OPAL-RT Technologies



Subject TBD

04:00-04:30 PM

First Name Last Name Job title
Company

Closing Session OPAL-RT

04:30 - 05:00 PM

Room : Lowther



Jean Belanger

President and CTO
Opal-RT Technologies